

PATENT

ATTORNEY DOCKET NO. 114596-29-000125BS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 09/672,440 Confirmation No.: 7408

Applicant: Ronak Patel, et al.

Title: MANAGING INSTRUCTION SIDE-EFFECTS

Filed: September 28, 2000

Art Unit: 2183

Examiner: E. Coleman

Atty. Docket: 114596-29-000125BS

Customer No. 38492

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

In accordance with 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant wishes to make of record the items listed on the accompanying Form PTO-1449. Applicant respectfully requests the Examiner to fully consider the items and independently ascertain their teaching before issuance of the next action, and to make them of record in the file. The Examiner is also requested to initial and return a copy of the enclosed Form PTO-1449 to evidence such consideration.

1. Applicant has listed publication dates on the attached Form PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated. Applicant reserves the right to establish the patentability of the claims over any information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

2. The references listed on the enclosed Form 1449 are references that have come to light in applications listed in the Information Disclosure Statement of February 2001. For many of the listed references, pertinence is mentioned in the respective application file in which the reference came to light.

I certify that this correspondence, along with any documents referred to therein, is being deposited with the United States Postal Service on September 7, 2005 as First Class Mail in an envelope with sufficient postage addressed to Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Applicant has not reviewed all of them in detail. Of those that have been reviewed, none of the references are believed to be any more pertinent than the references provided in earlier IDS' and Forms 1449. However, in an abundance of caution, Applicant requests that they be considered.

3. Pursuant to 37 C.F.R. § 1.98(d), for items other than U.S. patents and U.S. patent applications listed on the enclosed Form PTO-1449 for which a copy is not already made of record in this application, a copy was previously cited by or submitted to the Patent and Trademark Office in application Serial No. 09/239,194, filed January 28, 1999, Yates et al., Executing Programs for a First Computer Architecture on a Computer of a Second Architecture, in application Serial No. 09/322,443, filed May 28, 1999, Reese et al., Profiling of Computer Programs Executing in Virtual Memory Systems, or in application Serial No. 09/385,394, filed August 30, 1999, Yates et al., Computer for Executing Two Different Instruction Sets, or in application 09/626,325, filed July 26, 2000, Yates, et al., Computer with Two Operating Systems, each of which is an earlier application relied on for an earlier effective filing date under 35 U.S.C. § 120.

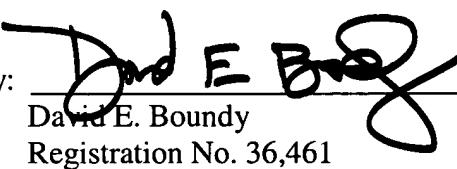
4. The three Intel references have been previously cited and considered. They are cited again here with improved bibliographic information.

5. This Information Disclosure Statement is being filed within three months of the filing date of a Request for Continued Examination. No fee is due, pursuant to 37 C.F.R. § 1.97(b)(1).

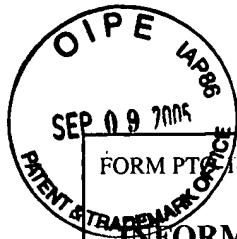
6. The Commissioner is hereby authorized to charge any additional fees that may be required for this Information Disclosure Statement, or credit any overpayment, to Deposit Account No. 23-2405, Order No. 114596-29-000125BS.

Respectfully submitted,
WILLKIE FARR & GALLAGHER LLP

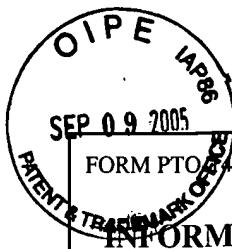
Dated: September 7, 2005

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FORM PTS-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. 114596-29-000125BS	SERIAL NO. 09/672,440		
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)			APPLICANT Ronak Patel, et al.			
			FILING DATE September 28, 2000	GROUP ART UNIT 2183		
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	4,722,050	01/26/1988	Lee	712	205	Mar. 27, 1986
	4,831,515	05/16/1989	Kamada	712	207	Feb. 10, 1986
	5,043,878	08/1991	Ooi	712	42	Aug. 9, 1989
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EXAMINER			DATE CONSIDERED			



 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 114596-29-000125BS	SERIAL NO. 09/672,440
INFORMATION DISCLOSURE CITATION		
(Use several sheets if necessary)		

U.S. PATENT DOCUMENTS

OTHER DOCUMENTS (*Including Author, Title, Date, Pertinent Papers, Etc.*)

		Intel Corporation, Intel Architecture Software Developer's Manual, Volume 1: Basic Architecture, pages 3-10 to 3-13, 7-1 to 7-3, 7-20 to 7-25; Volume 2: Instruction Set Reference, pages 3-60 to 3-63, 3-240 to 3-251; Volume 3: System Programming Guide, pages 11-1 to 11-3, 11-10 to 11-13, 14-1 to 14-3, 14-10 to 14-15 (1997)
		Intel Corporation, Pentium Processor Family Developer's Manual (1997), pages 1-1 to 1-6, 2-1 to 2-20
		Intel Corporation, Pentium Processor Family Developer's Manual (1997), vol. 3: Architecture and Programming Manual, pages 3-1 to 3-3, 3-10 to 3-13, 12-1 to 12-27, 14-1 to 14-30
		Peterson, "The Profile Naming Service," ACM Transactions on Computer Systems (TOCS), v.6 n.4, p.341-364 (Nov. 1988)

EXAMINER	DATE CONSIDERED
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